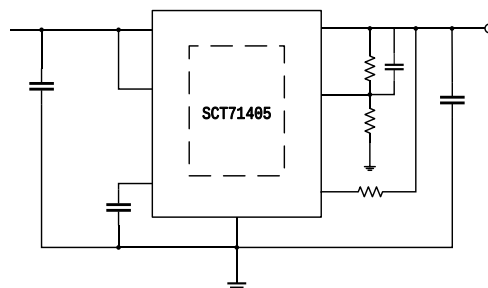
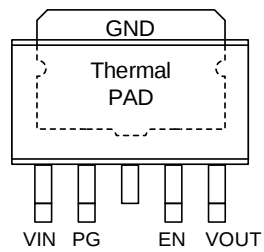
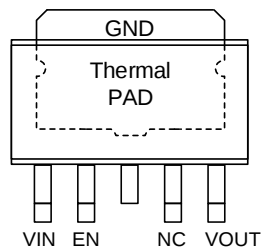
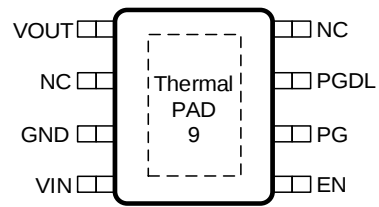
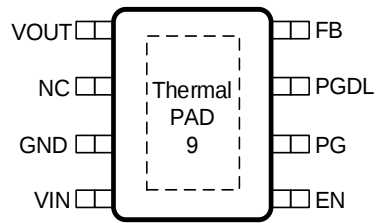


3V-40V Vin, 500mA, Ultra-Low Quiescent Current LDO with Power-Good



			5A00		
			F50B		
			F33B		
			5F50		
			5F33		
			F50A		



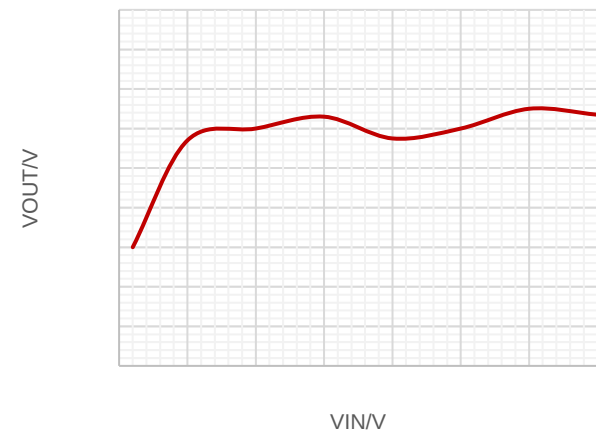
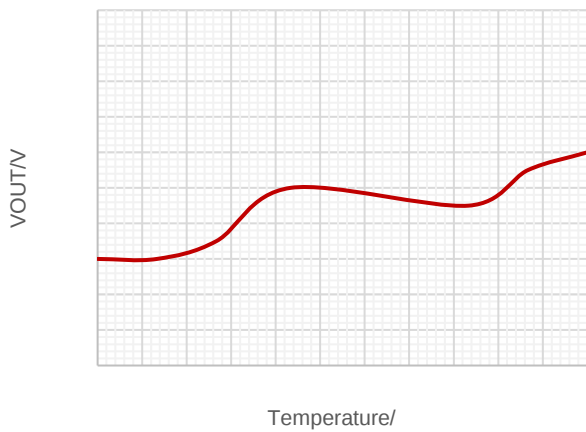
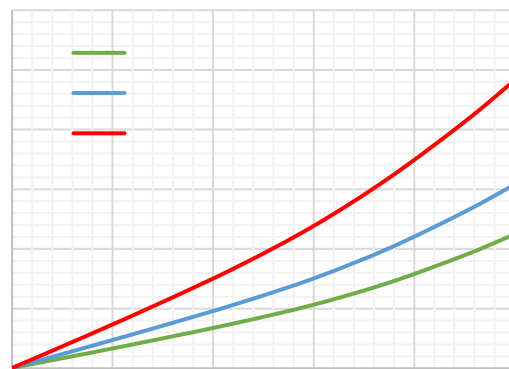
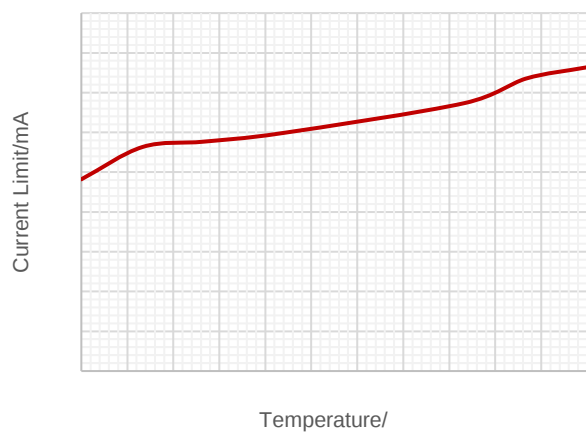
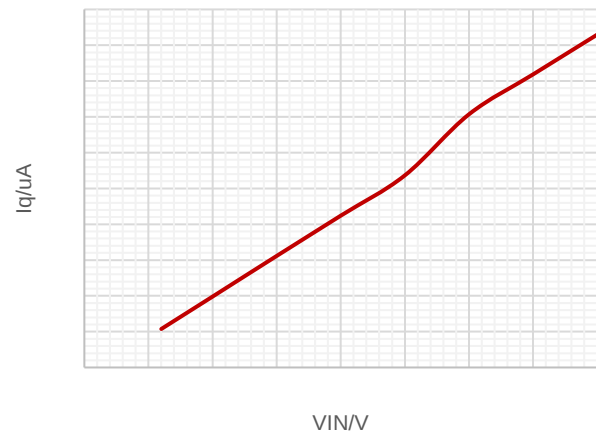
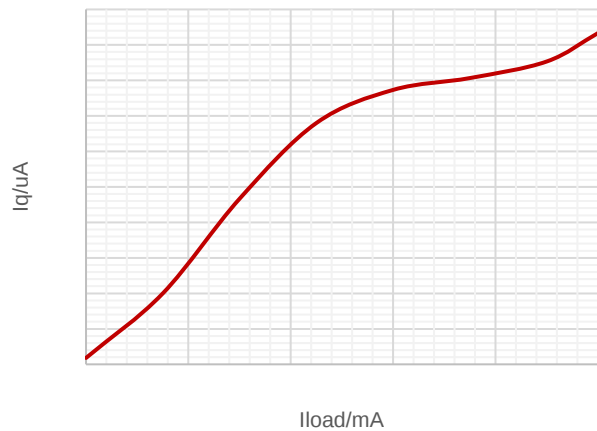
70Y40Y2† v † L À v.đLFROIRU PR

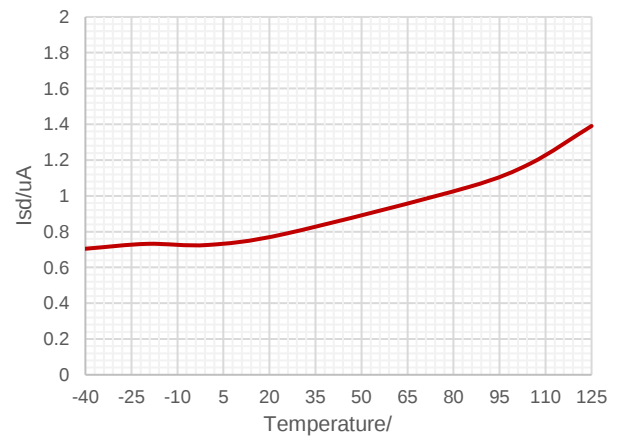
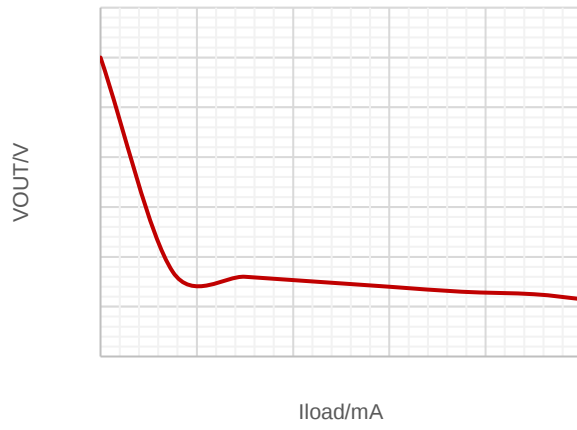
PARAMETER (4-layer)			UNIT

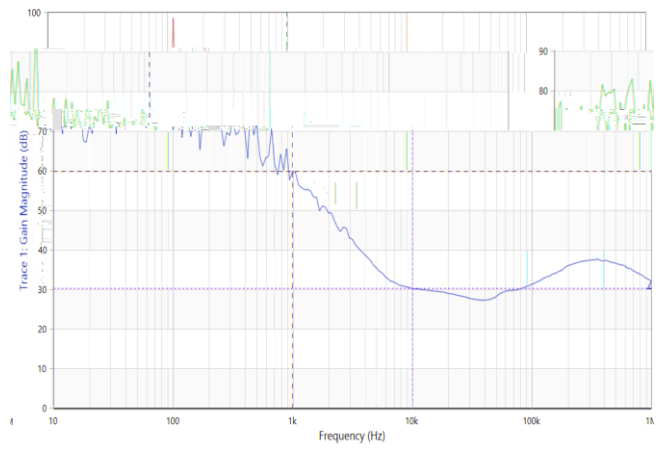
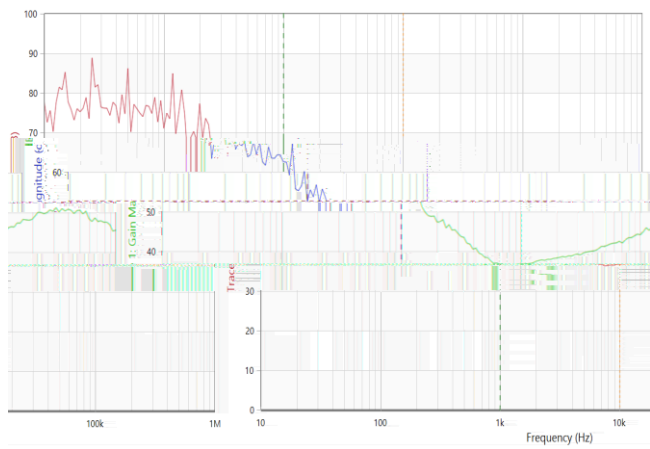
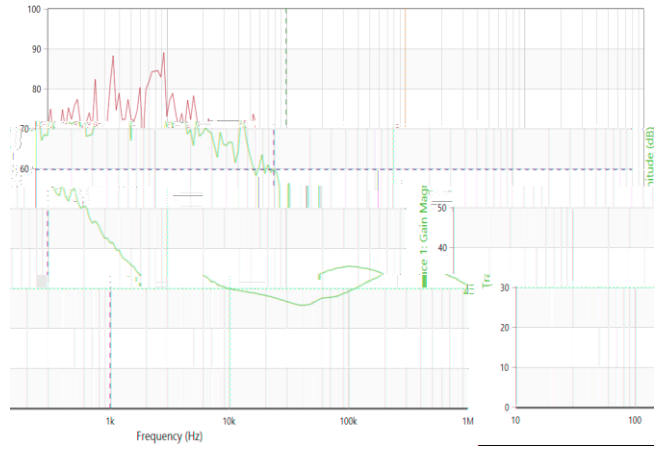
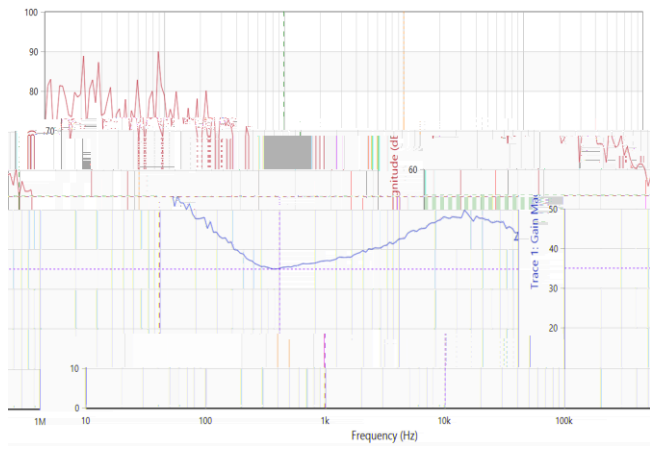
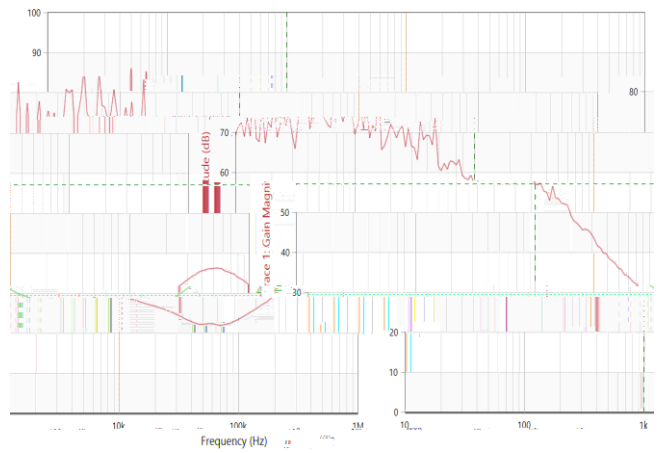
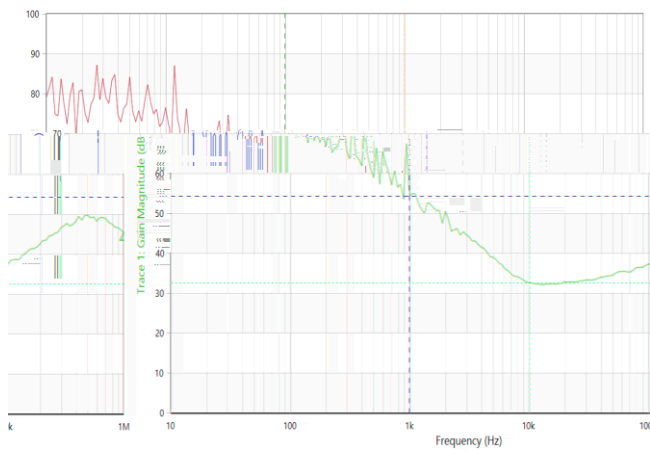
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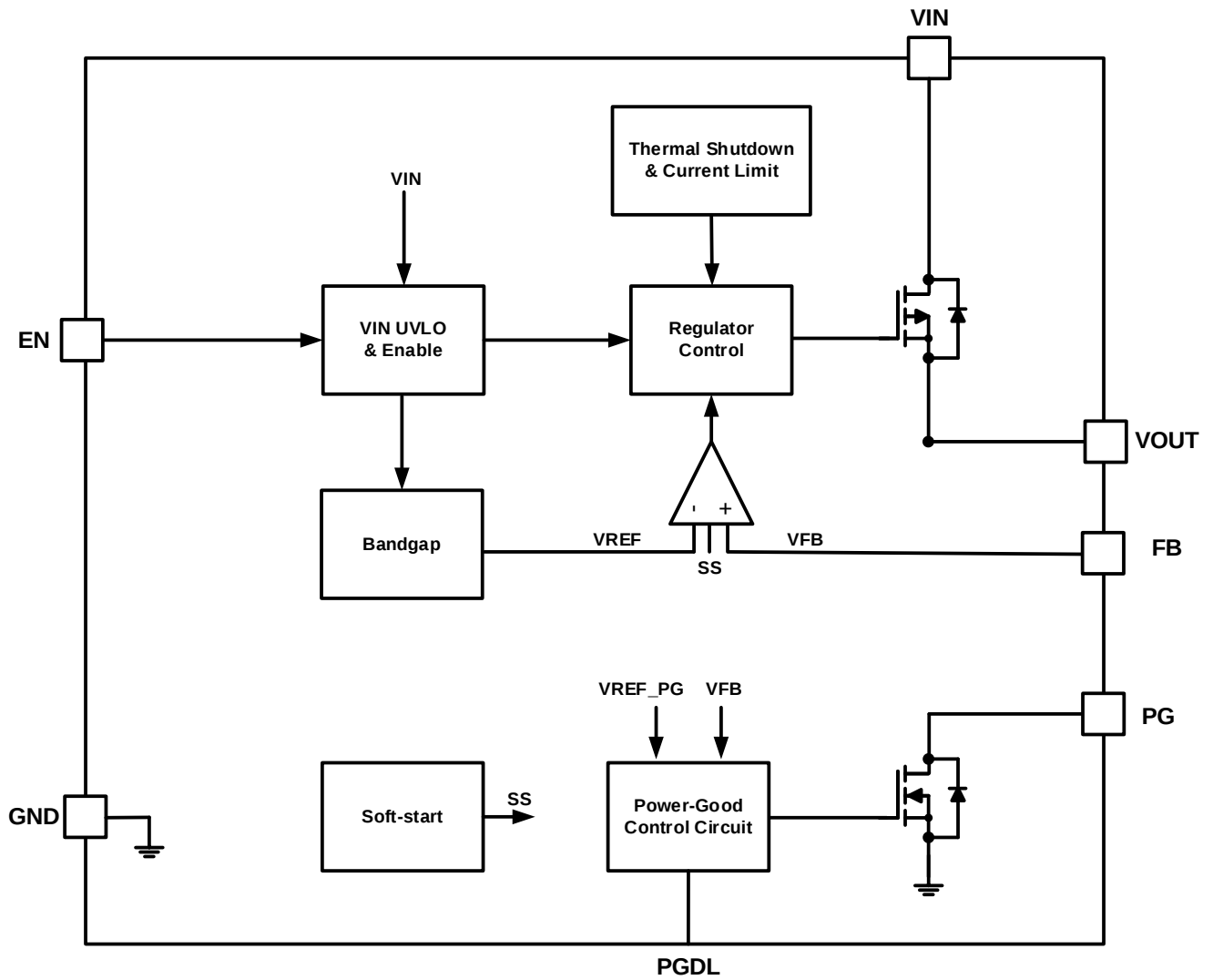
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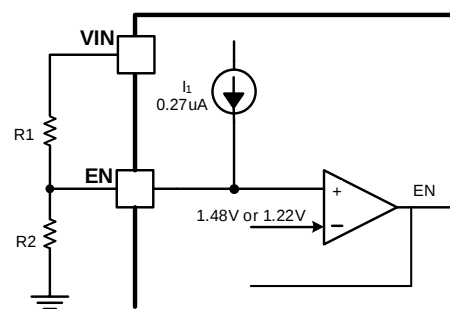


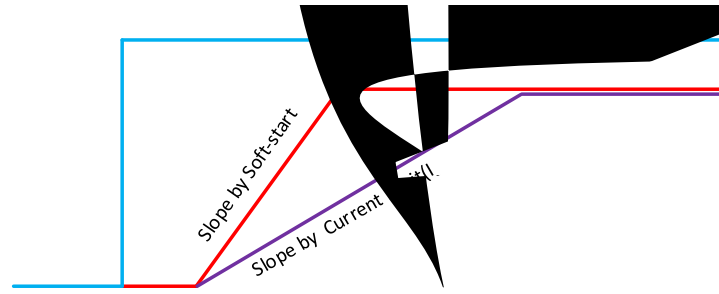


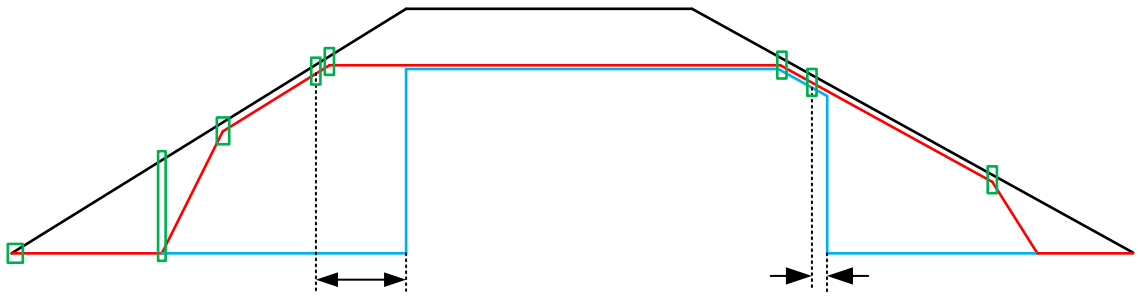


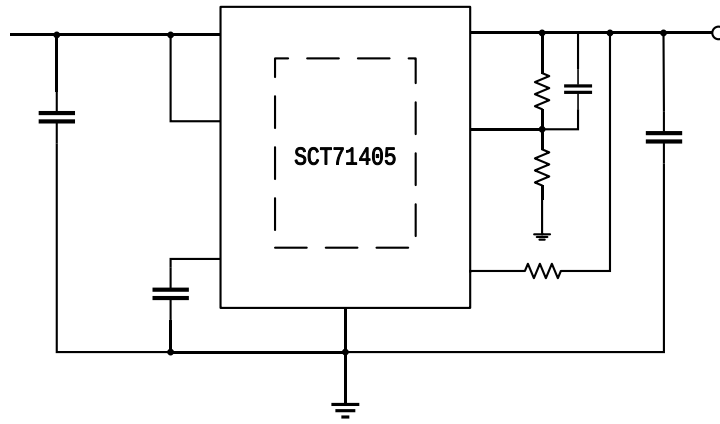


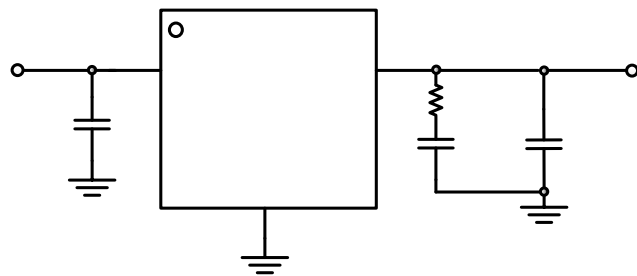


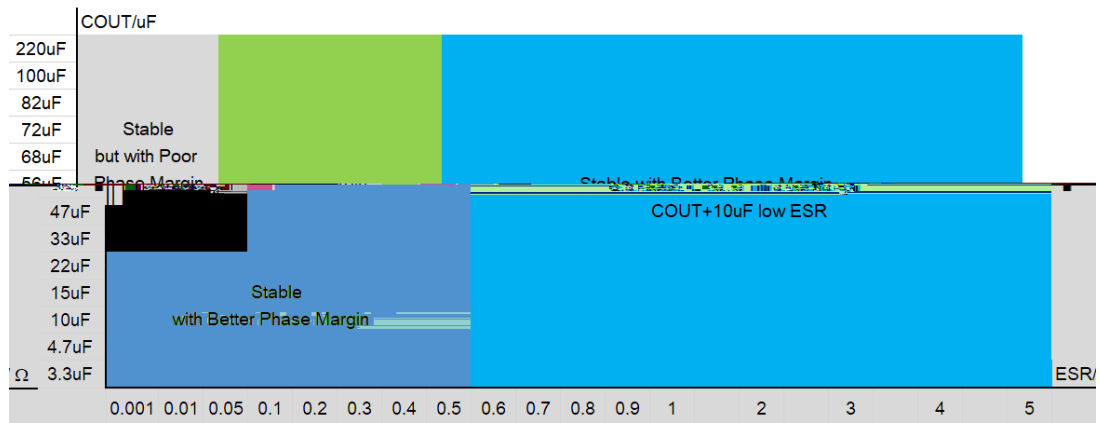


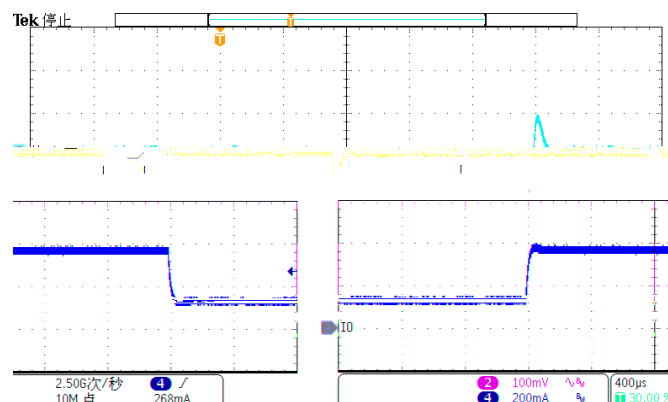
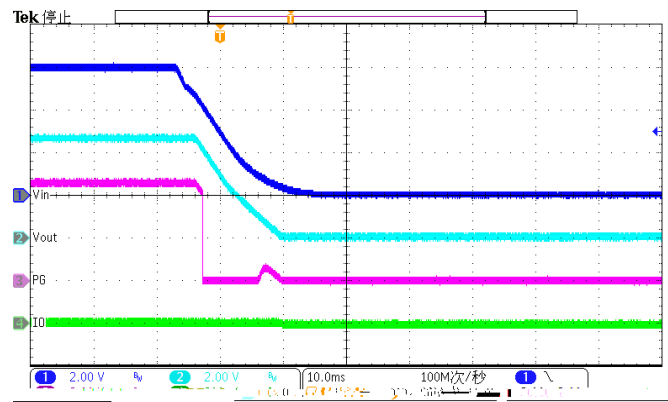
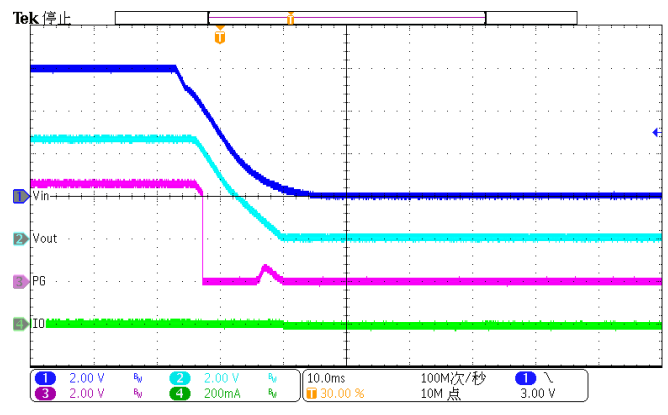


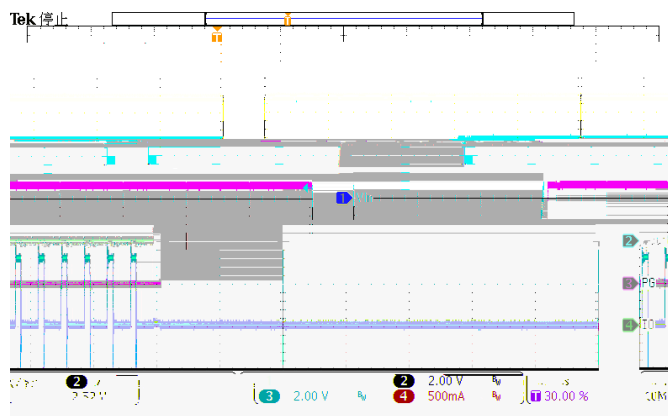
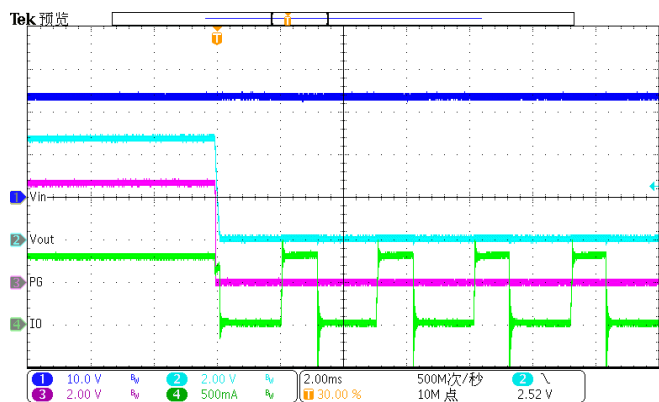
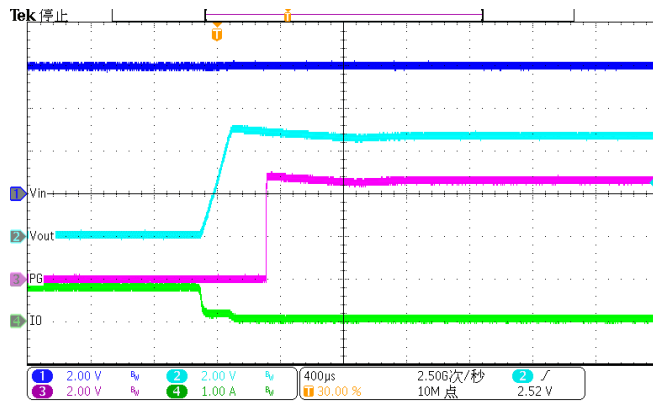
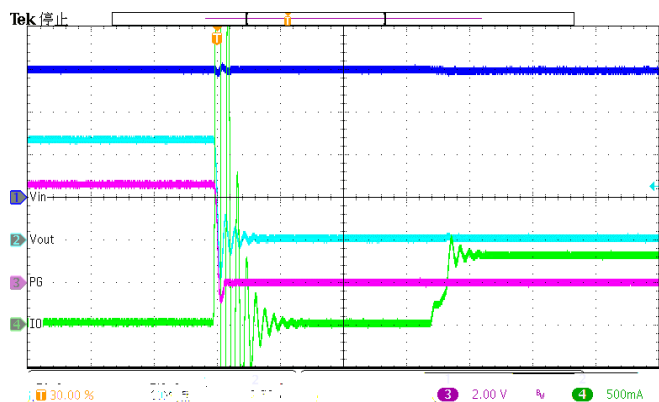
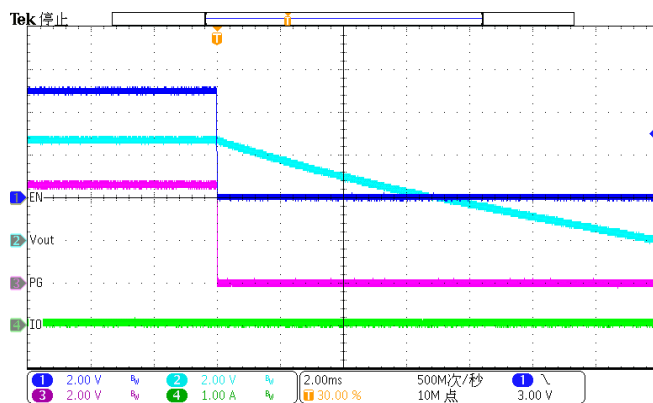
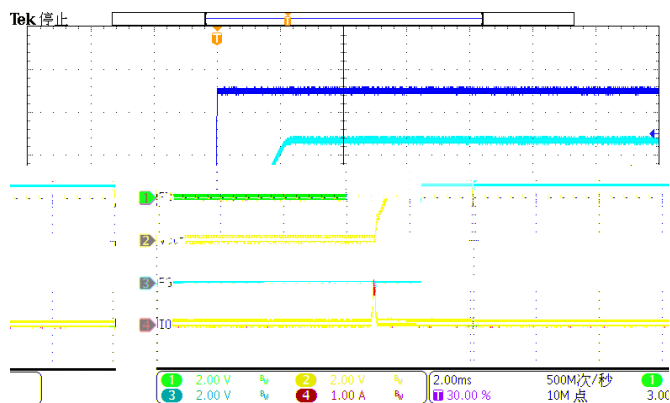




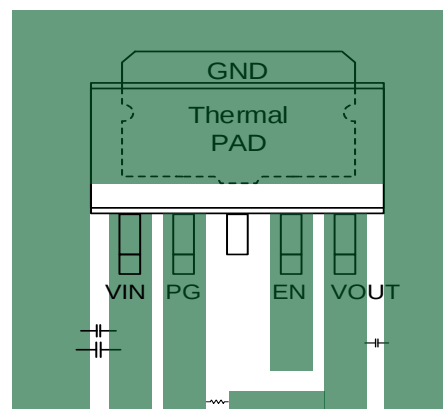
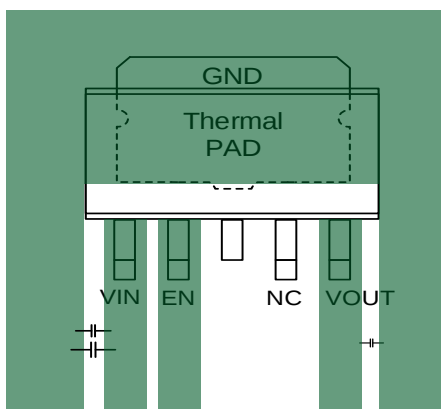
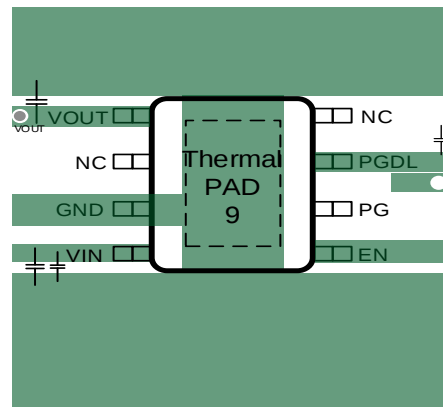
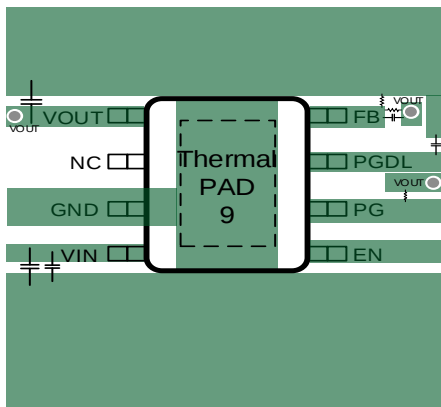




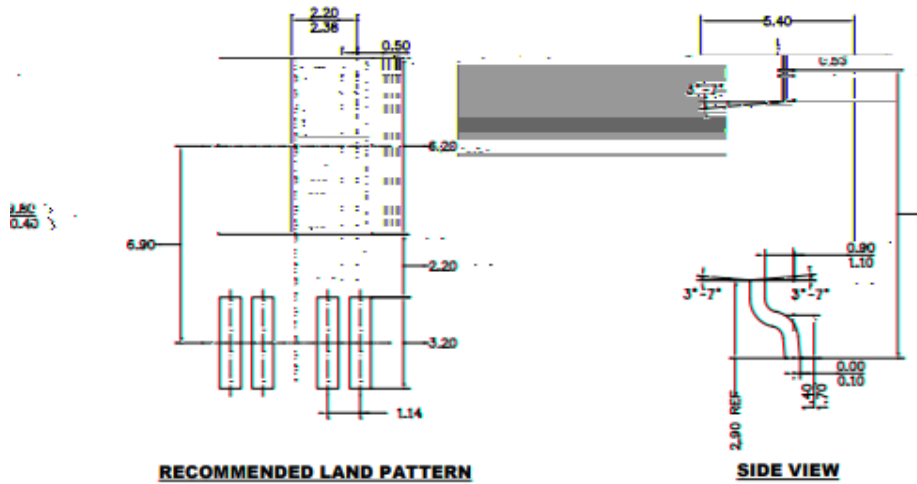
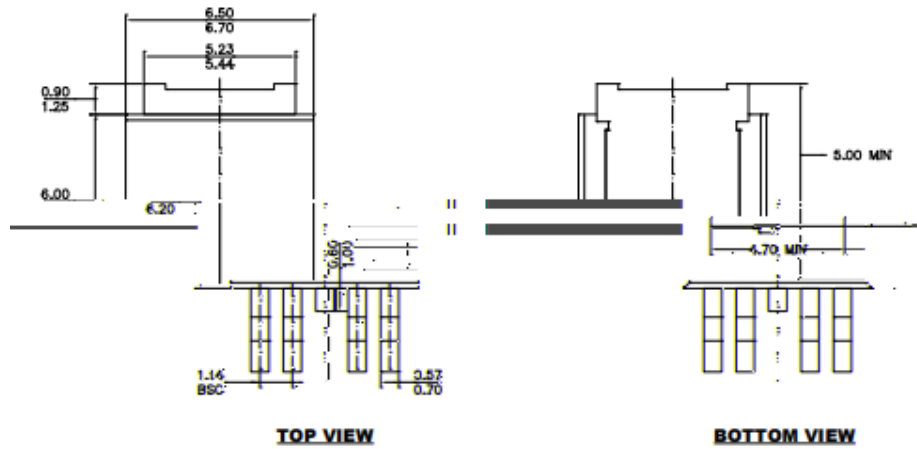




- 1.
- 2.
- 3.
- 4.
- 5.



PACKAGE OUTLINE DRAWING FOR TO252-4L
POD-0040 Revision 0.0



NOTE:

1) ALL DIMENSIONS ARE IN MILLIMETERS.
2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
4) DRAWING CONFORMS TO JEDEC C-TO-252.

1) ALL DIMENSIONS ARE IN MILLIMETERS.
2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
4) DRAWING CONFORMS TO JEDEC C-TO-252.
5) DRAWING IS NOT TO SCALE.

7.
8.
9.
10.
11.
