

3.5V-36V Vin, 4A, High Efficiency Synchronous Step-down DCDC Converter

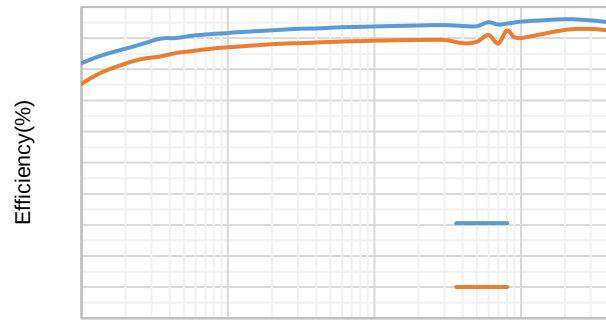
Z1ÊLQ SXW YROW

External bias option for improved efficiency
Parallel input path to minimize switch node ringing

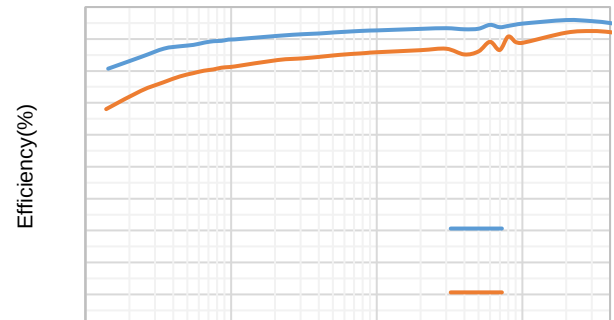
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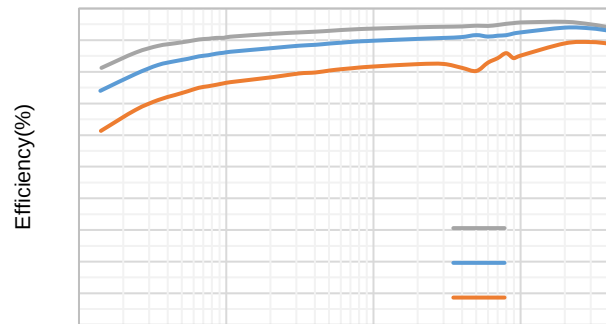
		POWER GOOD (% of FB voltage)		
		POWER BAD (% of FB voltage)		
		POWER BAD (% of FB voltage)		
		POWER GOOD (% of FB voltage)		
		VPull-Up = 5V		
		IPull-Up = 1 mA		



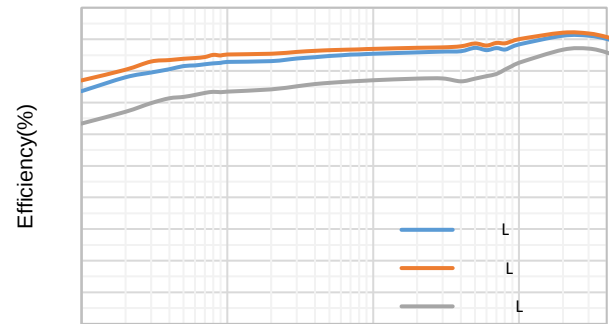
Output Current(A)



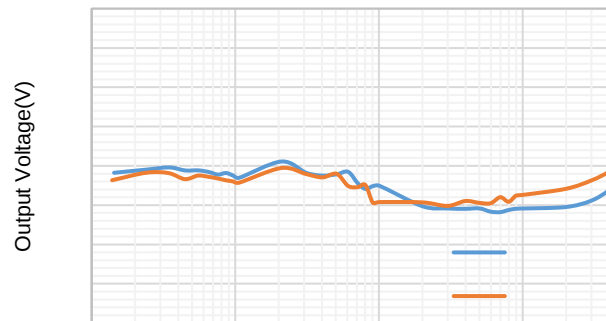
Output Current(A)



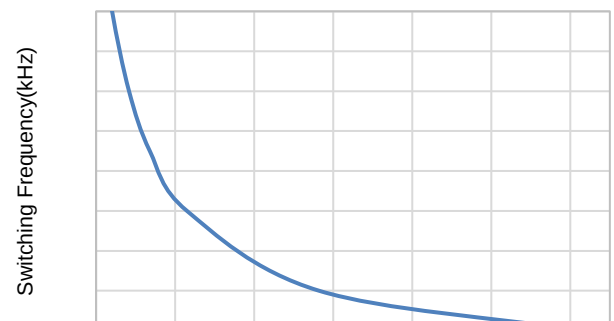
Output Current(A)

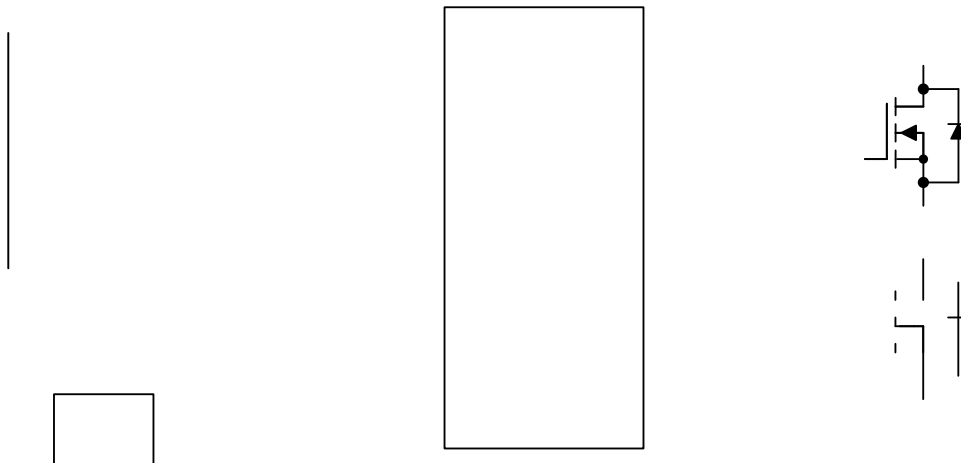


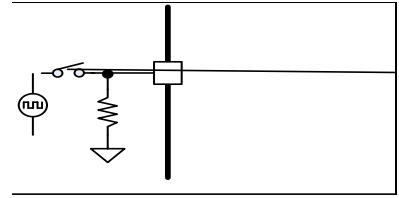
Output Current(A)

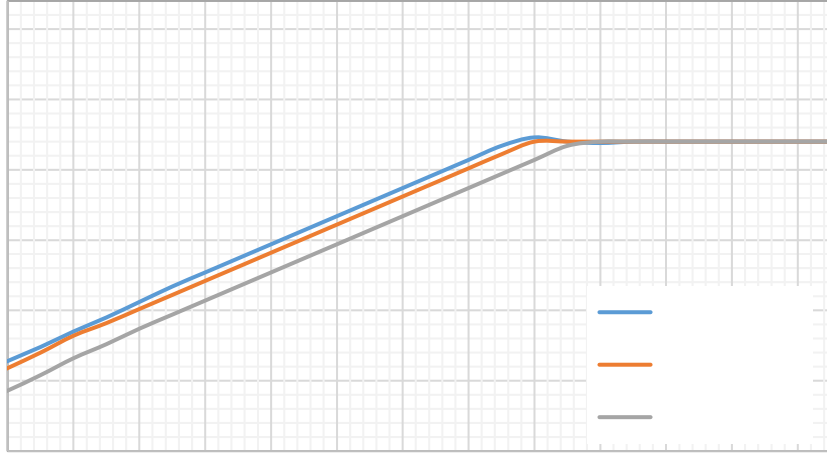


Output Current(A)

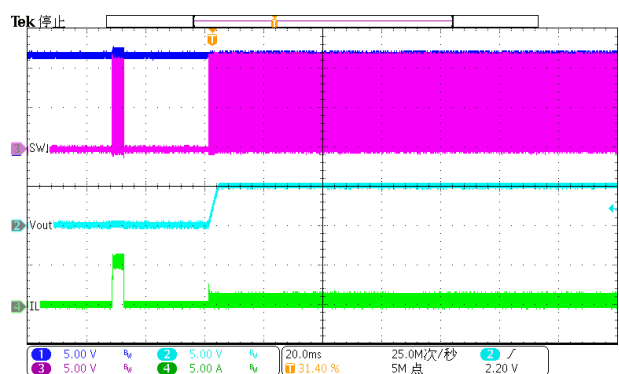
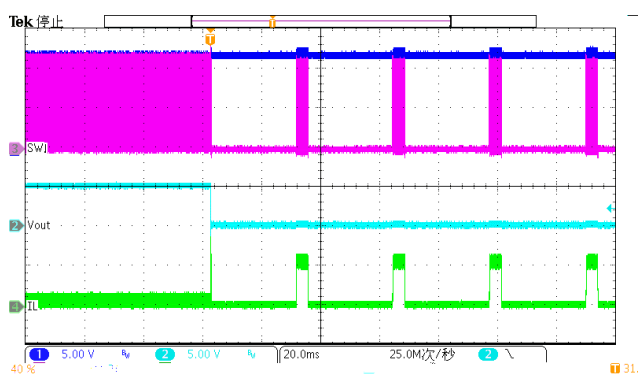
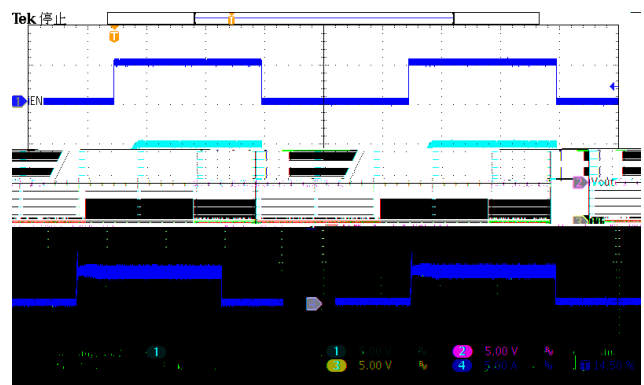
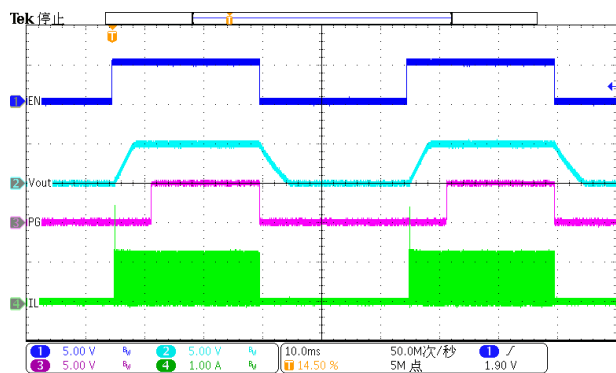
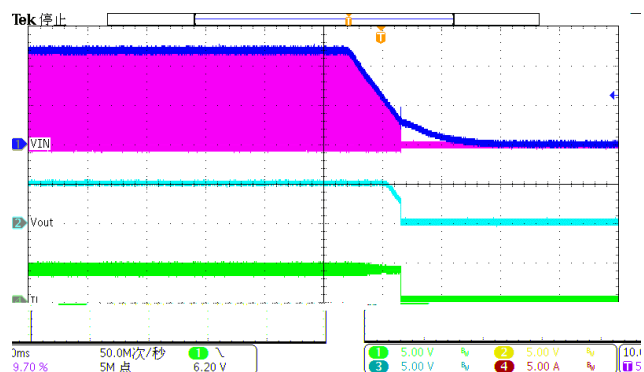
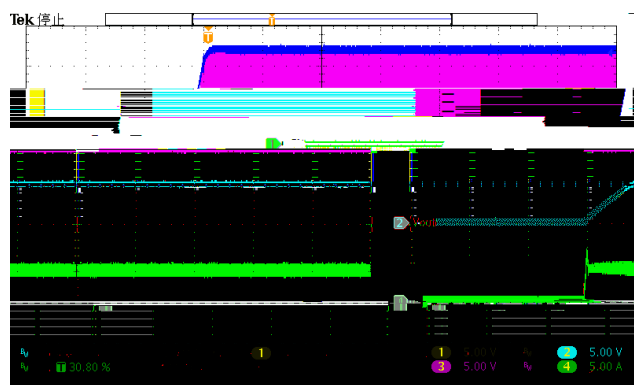




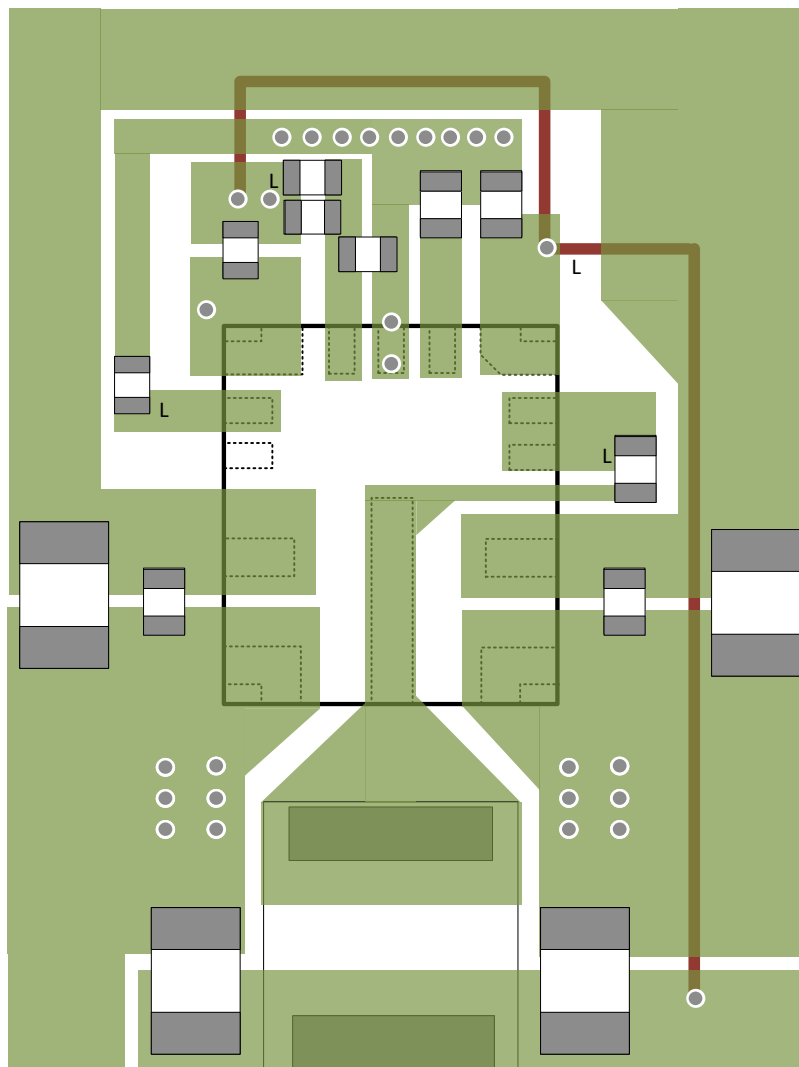


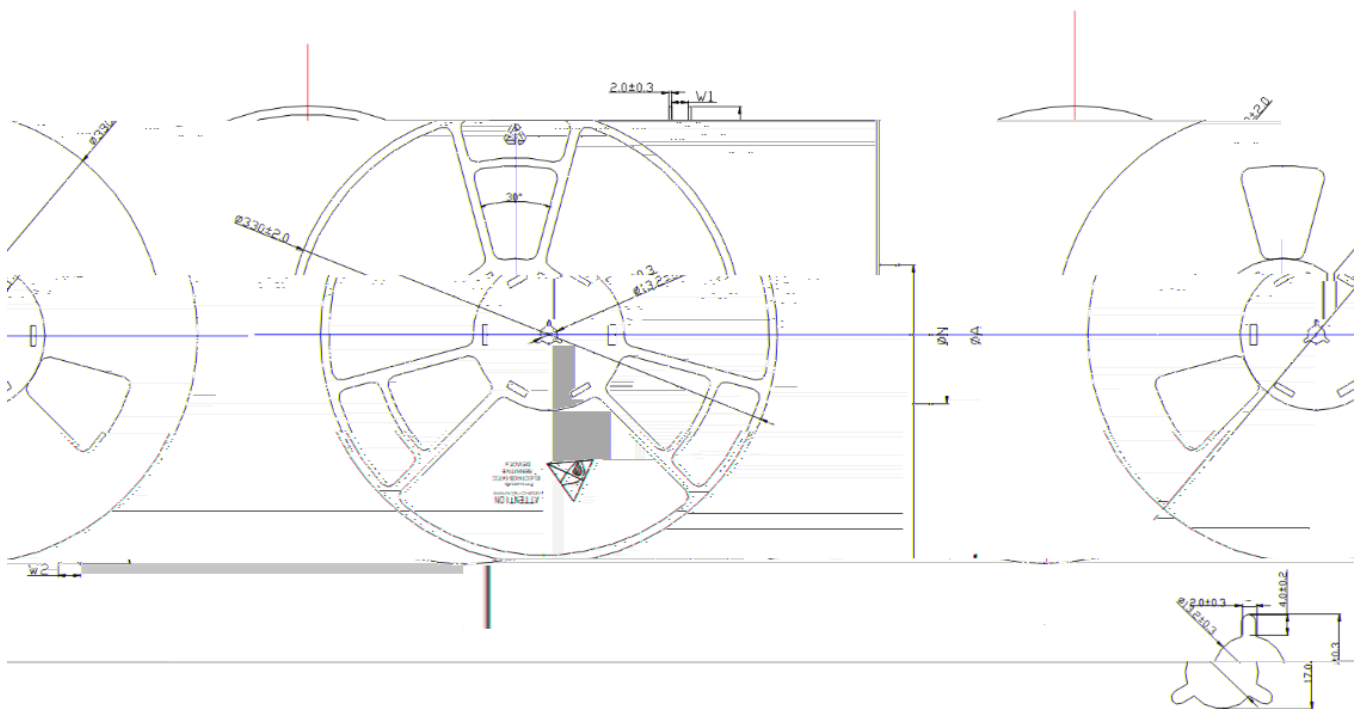


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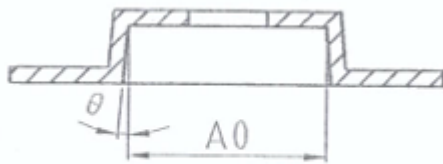
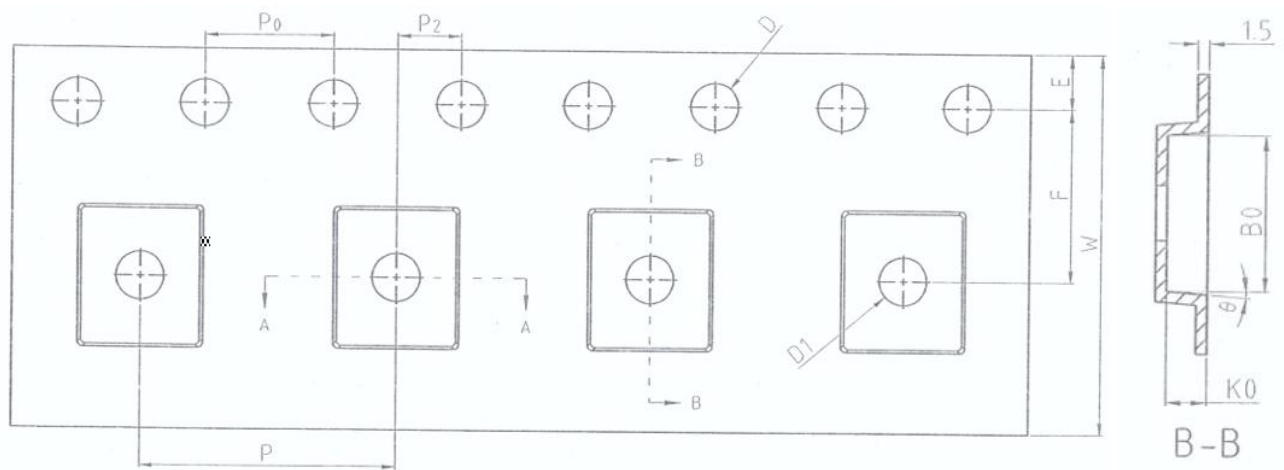


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- 1.
 - 2.
 - 3.
 - 4.
 - 5.
 - 6.

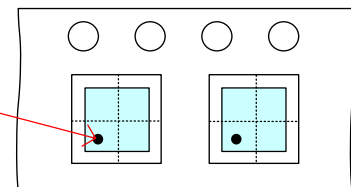




PRODUCT SPECIFICATIONS					
TYPE	WIDTH	ϕA	ϕN	W1 (Min)	W2 (Max)
12MM		330±2.0	100±1.0	12.4	19.4
16MM		330±2.0	100±1.0	16.4	19.4
24MM		330±2.0	100±1.0	24.4	19.4
32MM		330±2.0	100±1.0	32.4	19.4
44MM		330±2.0	100±1.0	44.4	19.4
51.4		330±2.0	100±1.0	51.4	19.4



A-A



外观	尺寸 (MM)
E	1.75 ± 0.10
F	5.50 ± 0.05
P2	2.00 ± 0.05
D	1.55 ± 0.05

外观	尺寸 (MM)
W	$12.00^{+0.30}_{-0.10}$
P	8.00 ± 0.10
A0	3.75 ± 0.10
B0	4.25 ± 0.10